Amendments to the Claims

Please replace the Claims as shown below:

1. (currently amended) A method of modifying a global electronic resource comprising:

displaying a list of parameter identifiers and respective parameter values

associated therewith for use in a first electronic design project;

selecting highlighting the global electronic resource via an input device wherein

the global electronic resource is associated with a first programmable microcontroller

circuit a parameter value of said respective parameter values that are displayed, said

parameter value corresponding to a parameter identifier of said list;

in response to said highlighting via said input device said parameter value,

displaying a plurality of possible parameter values which can be chosen for the global

electronic resource in response to said selecting said parameter identifier;

choosing one of the plurality of possible parameter values that are displayed as a

chosen parameter value for the global electronic resource said parameter identifier via

the input device; and

storing the chosen parameter value as a default global setting for use by a

second programmable microcontroller circuit electronic design project.

2. (currently amended) The method according to Claim 1 further comprising applying

the default global setting to the second programmable microcontroller circuit electronic

design project.

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plurality of possible parameter values includes displaying a pop-up list that comprises

the plurality of possible parameter values.

4. (currently amended) The method according to Claim 1 wherein said displaying the

plurality of parameter values includes displaying a window comprising the plurality of

parameter values parameter identifier corresponds to a CPU clock speed or a sleep

timer.

5. (currently amended) The method according to Claim 1 wherein the input device is a

computer mouse, a track ball, or a touch pad said parameter identifier corresponds to a

phase lock loop mode.

6. (currently amended) The method according to Claim 1 further comprising

propagating the chosen parameter value throughout said first programmable

microcontroller circuit wherein said parameter identifier corresponds to a clock divider

ratio or analog power.

(currently amended) A method of modifying a global electronic resource comprising:

displaying a list of parameter identifiers and respective parameter values

associated therewith for use in a first programmable microcontroller circuit;

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selecting highlighting a displayed value of the global electronic resource via an input device wherein the global electronic resource is associated with a first programmable microcontroller circuit a parameter value of said respective parameter values that are displayed via an input device, said parameter value corresponding to a parameter identifier of said list;

in response to said highlighting said parameter value, displaying a window comprising a plurality of possible parameter values which can be selected for the global electronic resource in response to said selecting the displayed value said parameter identifier;

selecting one of the plurality of possible parameter values as a selected parameter value for the global electronic resource parameter identifier via the input device; and

storing the selected parameter value as a default global electronic setting for use by a second programmable microcontroller circuit.

- 8. (currently amended) The method according to Claim 7 wherein the input device comprises a computer mouse the parameter identifier corresponds to a CPU clock speed or analog power.
- 9. (currently amended) The method according to Claim 7 wherein the input device comprises a track ball the parameter identifier corresponds to a phase lock loop mode.

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10. (currently amended) The method according to Claim 7 wherein the input device

comprises a touch pad the parameter identifier corresponds to a sleep timer.

11. (currently amended) The method according to Claim 7 wherein the window

comprises a pop-up list the parameter identifier corresponds to a clock divider ratio.

12. (currently amended) A system for selecting and using a current global parameter

value comprising:

a global resource menu configured to display a value of a global electronic

resource a list of parameter identifiers and respective parameter values associated

therewith for use in a first design project, and configured to display a plurality of global

possible parameter values which can be chosen for the global electronic resource a

parameter identifier of said list in response to the value being selected an input device

highlighting a parameter value of said respective parameter values that are displayed,

and configured to allow one of the plurality of global possible parameter values to be

chosen as the current global a current parameter value for said parameter identifier;

a global resource parameter selector coupled to the global resource menu and

configured to set the current global parameter value for an associated electronic

hardware resource; and

a global resource database coupled to the global resource parameter selector for

tracking a location within the associated electronic hardware resource and for storing

the current global parameter value as a default global setting for use among a plurality

of programmable microcontroller circuits in a second design project.

13. (currently amended) The system according to Claim 12 further comprising an input

device connected to the global resource menu for choosing one of the plurality of global

parameter values wherein said parameter identifier corresponds to a CPU clock speed

or a clock divider ratio.

14. (currently amended) The system according to Claim 13 Claim 12 wherein the input

device comprises a computer mouse said parameter identifier corresponds to analog

power.

15. (currently amended) The method according to Claim 13 Claim 12 wherein the input

device comprises a track-ball said parameter identifier corresponds to a phase lock loop

mode.

16. (currently amended) The method according to Claim 13 Claim 12 wherein the input

device comprises a touch pad said parameter identifier corresponds to a sleep timer.

17. (currently amended) In a design system for programming integrated circuits, a

method of processing global electronic design resources comprising:

displaying, in tabular form, a list of global electronic design resources parameter

names and respective global design parameter values associated therewith for use in a

first programmable microcontroller integrated circuit;

in response to a user selection of highlighting a global electronic design resource

parameter value of said respective parameter values that are displayed, displaying a

window comprising a plurality of possible values which can be selected for said global

electronic design resource a parameter name of said list;

in response to a user selection of a value of said plurality of possible values,

assigning said global electronic design resource to said value to said parameter name;

and

in response to the user selection of the value, storing said value of said global

electronic design resource parameter name to a default global setting for use in a

second programmable microcontroller integrated circuit.

18. (currently amended) The method as described in Claim 17 further comprising:

selecting said global electronic design resource; and

selecting said value wherein said parameter name corresponds to a CPU clock

speed or a clock divider ratio.

19. (currently amended) The method as described in Claim 18 Claim 17 wherein said

selectings are performed using a cursor control device wherein said parameter name

corresponds to analog power.

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20. (currently amended) The method as described in Claim 17 further comprising:

updating a memory resident database comprising said global electronic design

resources parameter names and associated parameter values; and

propagating said global electronic design resources and associated parameter

values across the first programmable microcontroller circuit wherein said parameter

name corresponds to a phase lock loop mode.

21. (currently amended) The method as described in Claim 17 wherein said method

further comprising:

propagating said global electronic design resources and associated parameter

values across the first programmable microcontroller-circuit parameter name

corresponds to a sleep timer.

22. (currently amended) The method as described in Claim 17 Claim 21 wherein said

window comprises a pop-up list.

23. (currently amended) A design system for programming integrated circuits

comprising:

a processor coupled to a bus; and

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a memory coupled to said processor, said memory containing instructions for

implementing a method of processing global electronic design resources, said method

comprising:

displaying, in tabular form, a list of global electronic design resources parameter

identifiers and respective global design parameter values associated therewith for use in

a first programmable microcontroller integrated circuit;

in response to a user selection of highlighting a global electronic design resource

parameter value of said respective parameter values that are displayed, displaying a

window comprising a plurality of possible values which can be selected chosen for said

global electronic design resource a parameter identifier of said list;

in response to a user selection of a <u>chosen</u> value of said plurality of <u>possible</u>

values, assigning said global electronic design resource to said chosen value to said

parameter identifier; and

in response to the user selection of the chosen value, storing said chosen value

of said global electronic design resource parameter identifier to a default global setting

for use in a second programmable microcontroller integrated circuit.

24. (currently amended) The design system as described in Claim 23 wherein said

method further comprises:

selecting said global electronic design resource; and

selecting said value parameter identifier corresponds to a CPU clock speed or a

sleep timer.

25. (currently amended) The design system as described in Claim 24 Claim 23 wherein said selectings are performed using a cursor control device parameter identifier corresponds to a phase lock loop mode.

26. (currently amended) The design system as described in Claim 23 wherein said method further comprises:

updating a memory resident database comprising said global electronic design resources and associated parameter values parameter identifier corresponds to analog power.

27. (currently amended) The design system as described in Claim 26 Claim 23 wherein said method further comprises:

propagating said global electronic design resources and associated parameter values across said first programmable microcontroller circuit parameter identifier corresponds to a clock divider ratio.

28. (currently amended) The design system as described in Claim 23 Claim 27 wherein said window comprises a pop-up list.

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